

```
1 MODULE dj_pld
2
3 TITLE 'Dohn Joe Proj'
4
5 SW[0..4] PIN 9,10,11,12,13 ISTYPE 'COM';
6
7 DATA[0..7] PIN 14,15,16,17,19,20,21,22,23 ISTYPE 'COM';
8
9 FAN_ON_IN PIN 24;
10 FAN_ON_OUT PIN 25 ISTYPE 'COM';
11 L_IRQ7 PIN 26 ISTYPE 'COM';
12
13 L_MSTR_RST PIN 31 ISTYPE 'COM';
14 L_TA PIN 32 ISTYPE 'COM';
15 L_TA_BDM PIN 33 ISTYPE 'COM';
16 R_W PIN 34 ISTYPE 'COM';
17 L_CS2 PIN 35;
18 L_CS3 PIN 36;
19 L_CS4 PIN 37;
20 STATUS_LED PIN 41;
21 BUFF_DIR PIN 42;
22 INVERTER_IN PIN 43;
23 INVERTER_OUT PIN 44;
24 M4EN_IN PIN 1;
25 M4EN_OUT PIN 2;
26 M3EN_IN PIN 3;
27 M3EN_OUT PIN 4;
28
29 DATA_OE_NODE NODE;
30
31 PLSI PROPERTY 'ISP ON';
32 PLSI PROPERTY 'PULLUP ON';
33
34 EQUATIONS
35
36 FAN_ON_OUT = !FAN_ON_IN;
37 FAN_ON_OUT.OE = 1;
38 INVERTER_OUT = !INVERTER_IN;
39 INVERTER_OUT.OE = 1;
40 M4EN_OUT = !M4EN_IN;
41 M4EN_OUT.OE = 1;
42 M3EN_OUT = !M3EN_IN;
43 M3EN_OUT.OE = 1;
44
45 STATUS_LED = !L_MSTR_RST;
46 STATUS_LED.OE = 1;
47
48 L_TA = L_TA_BDM;
49 L_TA.OE = 1;
50
51 WHEN (!L_CS2 # !L_CS4) THEN BUFF_DIR = !R_W
52 ELSE BUFF_DIR = 1; // 1 ON THIS LINE DRIVES A TO B
53
54 DATA_OE_NODE = (!L_MSTR_RST # !L_CS3);
55 //DATA_OE_NODE = (!L_MSTR_RST) # (!L_CS3 & R_W);
56
57 DATA0 = (L_MSTR_RST & !L_CS3 & SW0); // RESET 0
58 DATA0.OE = DATA_OE_NODE;
59 DATA1 = (L_MSTR_RST & !L_CS3 & SW1); // RESET 0
60 DATA1.OE = DATA_OE_NODE;
61 DATA2 = (!L_MSTR_RST) # (!L_CS3 & SW2); // RESET 1
62 DATA2.OE = DATA_OE_NODE;
63 DATA3 = (!L_MSTR_RST) # (!L_CS3 & SW3); // RESET 1 (X)
64 DATA3.OE = DATA_OE_NODE;
65 DATA4 = (L_MSTR_RST & !L_CS3 & SW4); // RESET 0
66 DATA4.OE = DATA_OE_NODE;
67 DATA5 = (!L_MSTR_RST) # (!L_CS3); // RESET 1 (X)
68 DATA5.OE = DATA_OE_NODE;
69 DATA6 = (!L_MSTR_RST) # (L_CS3); //these two inverted to id production version
70 DATA6.OE = DATA_OE_NODE;
71 DATA7 = (!L_MSTR_RST) # (L_CS3); //these two inverted to id production version
72 DATA7.OE = DATA_OE_NODE;
73
74 L_IRQ7 = (SW0 & SW1 & SW2 & SW3 & SW4);
75 L_IRQ7.OE = 1;
76
77 END
78
```