

```
1 REM *** This script is used to initialize the ColdFire processor
2 reset
3
4 REM *** Setting Module Base Address (SIM) ***
5 control 0xC0F 0x10000001
6
7 REM *** Setting Vector Base Register ***
8 control 0x801 0x00100000
9
10
11 REM *** RAMBAR0 code
12 control 0xC04 0x20000001
13 REM *** RAMBAR1 data
14 control 0xC05 0x20000801
15
16 REM *** ACRO-ACR3
17 control 0x004 0x00000000
18 control 0x005 0x00000000
19 control 0x006 0x00000000
20 control 0x007 0x00000000
21
22 REM *** CACR
23 control 0x002 0x00000000
24
25 mm.b 0x1000004C 0x00
26 mm.b 0x1000004D 0x00
27 mm.b 0x1000004E 0x00
28 mm.b 0x1000004F 0x00
29 mm.b 0x10000050 0x00
30 mm.b 0x10000051 0x00
31 mm.b 0x10000052 0x00
32 mm.b 0x10000053 0x00
33 mm.b 0x10000054 0x00
34 mm.b 0x10000055 0x00
35
36 REM *** IMR
37 mm.l 0x10000044 0x0000FFFE
38 REM *** SYPCR
39 mm.b 0x10000001 0x00
40
41
42 REM *** disable all other chip selects 1..7
43 mm.w 0x1000008C 0x0000
44 mm.w 0x10000096 0x0000
45 mm.l 0x10000090 0x00000000
46 mm.w 0x10000098 0x0000
47 mm.w 0x100000A2 0x0000
48 mm.l 0x1000009C 0x00000000
49 mm.w 0x100000A4 0x0000
50 mm.w 0x100000AE 0x0000
51 mm.l 0x100000AA 0x00000000
52 mm.w 0x100000B0 0x0000
53 mm.w 0x100000BA 0x0000
54 mm.l 0x100000B6 0x00000000
55 mm.w 0x100000BC 0x0000
56 mm.w 0x100000C6 0x0000
57 mm.l 0x100000C2 0x00000000
58 mm.w 0x100000C8 0x0000
59 mm.w 0x100000D2 0x0000
60 mm.l 0x100000CE 0x00000000
61 mm.w 0x100000D4 0x0000
62 mm.w 0x100000DE 0x0000
63 mm.l 0x100000DA 0x00000000
64
65 REM *** Chip Select 0 ***
66 mm.w 0x10000080 0x0000
67 mm.w 0x1000008A 0x1580
68 mm.l 0x10000084 0x007F0001
69
70 REM *** SDRAM INIT ***
71 mm.w 0x10000100 0x822F
72
73 REM *** BANK 0 ***
74 mm.l 0x10000108 0x04001304
75 mm.l 0x1000010C 0x007C0001
76 REM *** PRECHARGE SEQUENCE 0 ***
77 mm.l 0x10000108 0x0400130C
78 mm.l 0x04100000 0xA5A5A5A5
79 REM *** REFRESH SEQUENCE 0 ***
80 mm.l 0x10000108 0x04009304
```

```
81 REM *** wait for 8 refreshes
82
83 REM *** DISABLE BANK 1 ***
84 mm.l 0x10000114 0x00000000
85
86 REM *** Setting PAR all pins as I/O***
87 mm.w 0x10000004 0x0000
88 REM *** Setting PADDR ***
89 mm.w 0x10000244 0xFFFF5
90 REM *** Set PADAT
91 mm.w 0x10000248 0x0000
92
93 REM *** MODE REGISTER INIT SEQUENCE 0 ***
94 mm.l 0x10000108 0x04009344
95 mm.l 0x04080400 0x00000000
96
97 LOAD config.s19
98 REM *** Setting Stack Pointer ***
99 A7 0x04200000
100
101 VBR 0x04000000
102 PC 0x04000828
```